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5 a first termination resistor block; and
a second termination resistor block which
differs in configuration from said first termination
resistor block, and wherein:

10 | said termination resistor circuit is
switched between said first termination resistor block
and said second termination resistor block.

2. The termination resistor circuit as claimed in claim 1, wherein:

15 said first/termination resistor block is
of a symmetric load configuration comprising transistors
of the same conductivity type; and

said second termination resistor block is of a transfer gate configuration comprising transistors of different conductivity types.

20 3. The termination resistor circuit as claimed in
claim 1, wherein:

said first termination resistor block comprises a first transistor and a second transistor, both being of a first conductivity type; and

25 said second termination resistor block
comprises a third transistor of the first conductivity
type and a fourth transistor of a second conductivity
type which differs from said first conductivity type.

4. The termination resistor circuit as claimed in
claim 3, wherein a plurality of said first termination
resistor blocks and a plurality of said second
termination resistor blocks are respectively arranged in
parallel, and said plurality of first termination
resistor blocks and said plurality of second termination
resistor blocks are respectively connected for control in
an arbitrary manner.

5. The termination resistor circuit as claimed in

claim 4, wherein said first and second transistors are chosen to substantially be equal in size for each of said first termination resistor blocks so that said plurality of first termination resistor blocks have the same weight.

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6. The termination resistor circuit as claimed in claim 4, wherein said first and second transistors are chosen to have an appropriate size for each of said first termination resistor blocks so that said plurality of first termination resistor blocks have respectively chosen appropriate weights.

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7. The termination resistor circuit as claimed in claim 4, wherein said third and fourth transistors are chosen to be substantially equal in size for each of said second termination resistor blocks so that said plurality of second termination resistor blocks have the same weight.

8. The termination resistor circuit as claimed in claim 4, wherein said third and fourth transistors are chosen to have an appropriate size for each of said second termination resistor blocks so that said plurality of second termination resistor blocks have respectively chosen appropriate weights.

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9. The termination resistor circuit as claimed in claim 3, wherein said first transistor and said third transistor are replaced by one common transistor.

10. A signal transmission system comprising:
a transmitting circuit for transmitting a signal;

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a transmission line for transmitting the signal output from said transmitting circuit;

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block;

and

Sub B3

Sub A2

Sub B5

Sub B3

a second termination resistor block which differs in configuration from said first termination resistor block, and wherein:

5 said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

11. The signal transmission system termination as claimed in claim 10, wherein:

10 said first termination resistor block is of a symmetric load configuration comprising transistors of the same conductivity type; and

 said second termination resistor block is of a transfer gate configuration comprising transistors of different conductivity types.

12. The signal transmission system as claimed in claim 10, wherein:

 said first termination resistor block comprises a first transistor and a second transistor, both being of a first conductivity type; and

 said second termination resistor block comprises a third transistor of the first conductivity type and a fourth transistor of a second conductivity type which differs from said first conductivity type.

13. The signal transmission system as claimed in claim 12, wherein a plurality of said first termination resistor blocks and a plurality of said second termination resistor blocks are respectively arranged in parallel, and said plurality of first termination resistor blocks and said plurality of second termination resistor blocks are respectively connected for control in an arbitrary manner.

14. The signal transmission system as claimed in claim 13, wherein said first and second transistors are chosen to be substantially equal in size for each of said first termination resistor blocks so that said plurality of first termination resistor blocks have the same

weight.

5 15. The signal transmission system as claimed in claim 13, wherein said first and second transistors are chosen to have an appropriate size for each of said first termination resistor blocks so that said plurality of first termination resistor blocks have respectively chosen appropriate weights.

10 16. The signal transmission system as claimed in claim 13, wherein said third and fourth transistors are chosen to be substantially equal in size for each of said second termination resistor blocks so that said plurality of second termination resistor blocks have the same weight.

15 17. The signal transmission system as claimed in claim 13, wherein said third and fourth transistors are chosen to have an appropriate size for each of said second termination resistor blocks so that said plurality of second termination resistor blocks have respectively chosen appropriate weights.

20 18. The signal transmission system as claimed in claim 12, wherein said first transistor and said third transistor are replaced by one common transistor.

25 19. A signal transmission system comprising:
a transmission line for transmitting a signal;
a receiving circuit for receiving the signal transmitted through said transmission line; and
a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

30 a first termination resistor block;
and

35 a second termination resistor block which differs in configuration from said first termination resistor block, and wherein:

said termination resistor

Sub
B¹⁰

Sub
B¹⁴

Spec

Sub
A⁵

circuit is switched between said first termination resistor block and said second termination resistor block.

5 20. The signal transmission system termination as claimed in claim 19, wherein:

 said first termination resistor block is of a symmetric load configuration comprising transistors of the same conductivity type; and

10 said second termination resistor block is of a transfer gate configuration comprising transistors of different conductivity types.

 21. The signal transmission system as claimed in claim 19, wherein:

15 said first termination resistor block comprises a first transistor and a second transistor, both being of a first conductivity type; and

20 said second termination resistor block comprises a third transistor of the first conductivity type and a fourth transistor of a second conductivity type which differs from said first conductivity type.

25 22. The signal transmission system as claimed in claim 21, wherein a plurality of said first termination resistor blocks and a plurality of said second termination resistor blocks are respectively arranged in parallel, and said plurality of first termination resistor blocks and said plurality of second termination resistor blocks are respectively connected for control in an arbitrary manner.

30 23. A signal transmission system comprising:
 a transmitting circuit for transmitting out a signal;

 a transmission line for transmitting therethrough the signal output from said transmitting circuit;

35 a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

5 a first termination resistor block;
and

a second termination resistor block which differs in configuration from said first termination resistor block, and wherein:

10 said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

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